

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S58	2	("4786826").URPN.	USPAT	OR	OFF	2007/09/19 15:35
S61	20436	"323"/\$3.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/19 15:36
S60	6733	713/3\$2.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/19 15:36
S64	1	S59 same S62	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/19 15:37
S63	4836	13same S62	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/19 15:37
S59	2040	power adj chip	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/19 15:38
S66	52047	(provid\$3 suppl\$4 send\$3 deliver\$3) near3 (power current voltage) near5 load\$1	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/19 15:57
S65	68	S59 and (S60 S61)	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/19 15:57
S62	4836	(control\$4 near4 power) with (provid\$3 suppl\$4 send\$3) near3 load\$1	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/19 15:57
S69	43	S68 and (S60 S61)	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/19 15:58
S68	261	S66 with S67	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/19 15:58
S67	297080	(microprocessor processor microcontroller) near3 control\$4	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/19 16:14
S70	19	S67 with S59	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/19 17:27
S72	19655	323/2\$2.ccls. 323/3\$2.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/21 13:57
S71	6751	713/3\$2.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/21 13:57

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S73	626516	(microprocessor processor microcontroller controller) near4 control\$4	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/21 13:58
S77	1173	S75 and S71	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/21 13:59
S76	2119	S75 and (S71 S72)	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/21 13:59
S75	37165	S73 with S74	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/21 13:59
S74	912845	(provid\$3 send\$3 suppl\$4 deliver\$3) near3 (power current voltage)	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/21 13:59
S79	8	S76 and (power adj chip)	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/21 14:00
S78	963	S75 and S72	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/21 14:00
S81	22	S76 and (power adj (chip ic))	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/21 14:01
S82	18	S76 and (power adj2 (chip ic circuit)) with (test\$3 diagnos\$3)	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/21 14:02
S80	929	S76 and (power adj2 (chip ic circuit))	US-PGPUB; USPAT; EPO	OR	OFF	2007/09/21 14:02
L1	2474	713/3\$2.ccls.	US-PGPUB	OR	OFF	2007/09/26 11:57
L4	1	3.clm.	US-PGPUB	OR	OFF	2007/09/26 11:58
L2	828	power adj chip	US-PGPUB	OR	OFF	2007/09/26 11:58
L3	3	2 with program adj controlled adj unit	US-PGPUB	OR	OFF	2007/09/26 11:59
L5	4	2 with diagnostic	US-PGPUB	OR	OFF	2007/09/26 12:00
L8	3	2 with 7	US-PGPUB	OR	OFF	2007/09/26 12:03
L7	40	"load control data"	US-PGPUB	OR	OFF	2007/09/26 12:03
L6	1	5.clm.	US-PGPUB	OR	OFF	2007/09/26 12:03
L9	1	8.clm.	US-PGPUB	OR	OFF	2007/09/26 12:12
L12	3	10 with 2	US-PGPUB	OR	OFF	2007/09/26 12:14
L11	0	10.clm.	US-PGPUB	OR	OFF	2007/09/26 12:14
L10	7	microsecond adj bus	US-PGPUB	OR	OFF	2007/09/26 12:14